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Dr Nikolaos Voros is Professor at Electrical and Computer Engineering Department of the University of Peloponnese and director of the Embedded System Design and Applications laboratory. He has published more than 120 scientific publications in prestigious international conferences and journals, while he has published scientific books both in Greek and English. Currently, he is the coordinator of Horizon 2020 SMART4ALL, while he has participated as scientific coordinator in many national and European projects (Horizon 2020, FP7). He has organized five high quality international conferences (IEEE ISVLSI 2010, RecoSoC 2012, FPL 2014, IEEE ISVLSI 2017, ARC 2018).